CALL FOR PAPERS
PDP 2026

34th Euromicro/IEEE
International Conference on Parallel, Distributed, and Network-Based Processing

Cluj-Napoca, Romania
25-27 March 2026

https://pdp2026.org/

The PDP conference stands as a premier European and international conference that comprehensively addresses all facets of parallel and distributed processing. Encompassing a spectrum from foundational theory to practical implementation, it spans systems of all scales—from small configurations to the largest infrastructures.
PDP addresses fundamental computational challenges alongside robust applications and delves into architecture, compiler, language, interface design and deployment, tools, support infrastructures, and performance optimization.

Important dates

Paper submissions: ~~October 26th, 2025~~ November 9th, 2025
Author notification: ~~December 7th, 2025~~ December 21st, 2025
Camera-ready: January 25th, 2026

Topics

We invite submissions of high-quality, novel, and original research results in areas of parallel and distributed computing, including but not limited to:

**** Algorithms:*** resource-aware and power-efficient algorithms; real-time and fault-tolerant distributed/parallel algorithms; graph and network algorithms.

**** Applications:*** numerical and scientific applications with multi-level parallelism; applications with computations over irregular domains; models and methods to enhance functional/non-functional application characteristics.

*** Data-centric Processing:** scientific workflows; large-scale data processing; large-scale data management; scalable and next-generation storage systems; I/O performance tuning, benchmarking, and middleware; FAIR/open data systems.

**** Distributed AI***: Federated Learning; Distributed Learning; AI at scale; training of LLMs; benchmarking for AI workloads.

* ***Distributed Computing:*** cluster, grid, fog/edge, mobile and cloud systems; Service-oriented processing; stochastic and approximate computing; cost, security, energy, and other non-functional requirements models and frameworks.

* ***Parallel Computing***: accelerator-based systems inc. GPU, FPGA, neuromorphic and post-CMOS devices; embedded parallel systems; dependability, survivability, and fault-tolerance; methodologies, benchmarking/metrics, performance analysis and tools.

* ***Programming Models and Tools***: programming languages, compilers, middleware and OS; libraries, runtime, and systems software; notations; performance prediction and analysis; simulation and modelling of parallel/distributed systems.

* ***HPC state of practice:*** Managing systems and storage; managing facilities; HPC project management; moving, managing and sharing data HPC in the cloud; networking and cybersecurity; HPC training and education strategies; application workflows; system benchmarking.

* ***Systems and Architectures:*** high data throughput and streaming architectures; memory organization; service-oriented architectures; heterogeneous and hybrid systems; resource management; post-CMOS architectures inc. quantum, neuromorphic, and others.

Research paper submission guidelines

Submissions must be in PDF format and should not exceed eight pages for long papers and four pages for short contributions. All submissions must follow the IEEE Conference Proceedings Format (double-column, 10pt size fonts) available at https://www.ieee.org/conferences/publishing/templates.html. Submission should be done via EasyChair – https://easychair.org/conferences?conf=pdp2026.

The review process is double-blind, and all papers need to be “best-effort” anonymized. We strongly encourage making code and data available anonymously (e.g., in an anonymous GitHub repository via Anonymous GitHub or in a Dropbox folder).

At least one author of each accepted paper must register for the PDP 2026 and present the paper.

Contact: <pdp2026@cs.ubbcluj.ro>